

(Claims Renumbered)  
NOT TO SCAN

D. AMENDMENTS TO THE CLAIMS

Claims 1-14, 18, 20-28, 32, and 34 (Cancelled)

15. (Currently Amended) An electrically addressable data storage unit having a plurality of data storage arrays on a substrate, each array having multiple coplanar data storage cells for recording, addressing and reading of data, comprising a plurality of address lines, each address line being in common with predetermined multiple arrays on the substrate, and further comprising a plurality of power lines, each being separately connected to one of the multiple arrays. 2

16. (Original) The data storage unit of claim 15, wherein the plurality of address lines comprises a plurality of row address lines, each row address line being in common with the rows of predetermined multiple arrays on the substrate. 1

3 17. (Original) The data storage unit of claim 15, wherein the plurality of address lines comprises a plurality of column address lines, each column address line being in common with the columns of predetermined multiple arrays on the substrate. 1

4 19. (Currently Amended) The data storage unit of claim ~~18-15~~ and further comprising a controller connected to the plurality of power lines and address lines to selectively address a data storage cell in a selected array while turning off the data storage cells in all unselected arrays.

5 29. (Currently Amended) A method for recording, addressing and reading of data in an electrically addressable data storage unit having a plurality of data storage arrays, each array having a plurality of coplanar data storage cells, comprising connecting a plurality of address lines so that each address line is in common with predetermined multiple arrays on the substrate, and further comprising connecting a plurality of power lines so that each power line is separately connected to one of the multiple arrays. 5

6 30. (Original) The method of claim 29 wherein the step of connecting the plurality of address lines comprises connecting a plurality of row address lines so that each row address line is in common with a column of the predetermined multiple arrays on the substrate.

7 31. (Original) The method of claim 29 wherein the step of connecting the plurality of address lines comprises connecting a plurality of column address lines so that each column address line is in common with a row of the predetermined multiple arrays on the substrates.

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~~33.~~ (Currently Amended) The method of claim ~~32-29~~ and further comprising connecting a controller to the plurality of power lines and address lines to selectively address a data storage cell in a selected array while turning off the data storage cells in all unselected arrays.